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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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First Named Inventor	Hideyuki WADA
Art Unit	2894
Examiner Name	Andres R. Munoz
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U.S. PATENTS						
Examiner Cite No Patent Number Kind Code ¹ Issue Date Name of Patentee or Wi					Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	

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I Initials: I I Applicant of cited i locument I					Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		

	FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No	Foreign Document Number ³	Country Code ²	Kind Code ⁴	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T⁵
	1.	2002-373957	JP	А	2002-12-26	SHINKO ELECTRIC IND CO LTD		Abstract
	2.	2002-016212	JP	Α	2002-01-18	SEIKO EPSON CORP		Abstract
	3.	05-029537	JP	А	1993-02-05	FUJITSU LTD		Abstract

NON-PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city, and/or country where published.	T⁵		

EXAMINER SIGNATURE					
Examiner Signature	/Andres Munoz/	Date Considered	04/07/2011		

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ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /A.M./